

## AMENDMENTS TO CLAIMS

- Please amend pending claims 1, 6, 11, and 16, as indicated below. A complete listing of all claims and their status in the application are as follows:

1. (currently amended) A method of assembling a semiconductor package with stacked dies comprising:

providing a substrate;

attaching a first die to the substrate;

electrically connecting the first die to the substrate;

attaching a heat sink to the first die;

electrically connecting the heat sink to the substrate using a number of bonding wires;

the heat sink comprising a body portion, an undercut portion around a periphery thereof, and a plurality of legs integrally formed with the undercut portion;

attaching the plurality of legs to the substrate;

attaching a second die to the heat sink;

electrically connecting the second die to the substrate; and

encapsulating the first die, the heat sink, and the second die.

2. (original) The method of assembling a semiconductor package with stacked dies as claimed in claim 1 wherein:

electrically connecting the first die to the substrate uses a number of bonding wires;  
and

attaching a heat sink attaches a heat sink that extends laterally over the number of bonding wires.

3. (original) The method of assembling a semiconductor package with stacked dies as claimed in claim 1 wherein attaching a heat sink attaches a heat sink that is electrically grounded.

4. (original) The method of assembling a semiconductor package with stacked dies as claimed in claim 1 wherein attaching a heat sink attaches a heat sink that has an electrically conductive coating, further comprising:

connecting the second die to the electrically conductive coating; and

connecting the electrically conductive coating to a ground plane.

5. (original) The method of assembling a semiconductor package with stacked dies as claimed in claim 1 wherein attaching a heat sink attaches a heat sink that extends laterally beyond the edges of the second die.

6. (currently amended) A method of thermally enhancing a semiconductor package with a stack of dies comprising providing a heat sink between dies in the stack; the heat sink having a body portion, an undercut portion around a periphery thereof, ~~and~~ a plurality of legs integrally formed with the undercut ~~portion~~ portion, and electrically connected using a number of bonding wires to the stack of dies.

7. (original) The method of thermally enhancing a semiconductor package with a stack of dies as claimed in claim 6 wherein:

providing a heat sink attaches a heat sink that extends laterally over the lower die to which the heat sink is attached.

8. (original) The method of thermally enhancing a semiconductor package with a stack of dies as claimed in claim 6 wherein providing a heat sink attaches a heat sink that is electrically grounded.

9. (original) The method of thermally enhancing a semiconductor package with a stack of dies as claimed in claim 6 wherein providing a heat sink attaches a heat sink that has an electrically conductive coating, further comprising:

connecting one of the dies in the stack of dies to the electrically conductive coating;  
and  
connecting the electrically conductive coating to a ground plane.

10. (original) The method of thermally enhancing a semiconductor package with a stack of dies as claimed in claim 6 wherein providing a heat sink attaches a heat sink between each adjoining pair of dies in the stack of dies.

11. (currently amended) A semiconductor package with stacked dies comprising:  
a substrate;  
a first die attached to the substrate;  
the first die being electrically connected to the substrate;  
a heat sink attached to the first die;  
a number of bonding wires electrically connecting the heat sink to the substrate;

the heat sink having a body portion, an undercut portion around a periphery thereof,  
and a plurality of legs integrally formed with the undercut portion;  
the plurality of legs attached to the substrate;  
a second die attached to the heat sink and electrically connected to the substrate; and  
an encapsulant over the first die, the heat sink, and the second die.

12. (original) The semiconductor package with stacked dies as claimed in claim 11 further comprising:

a number of bonding wires electrically connecting the first die to the substrate; and  
wherein:

the undercut of the heat sink extends laterally over the number of bonding wires.

13. (original) The semiconductor package with stacked dies as claimed in claim 11 wherein the heat sink is electrically grounded.

14. (original) The semiconductor package with stacked dies as claimed in claim 11 wherein:

the heat sink has an electrically conductive coating connected to a ground plane on the  
substrate; and

the second die is connected to the electrically conductive coating.

15. (original) The semiconductor package with stacked dies as claimed in claim 11 wherein the undercut of the heat sink extends laterally beyond the edges of the second die.

16. (currently amended) A thermally enhanced semiconductor package with a stack of dies comprising:

a heat sink between dies in the stack;

the heat sink having a body portion, an undercut portion around a periphery thereof,

and a plurality of legs integrally formed with the undercut ~~portion-portion~~; and

a number of bonding wires electrically connecting the heat sink to the dies in the stack.

17. (original) The thermally enhanced semiconductor package with a stack of dies as claimed in claim 16 wherein:

the undercut of the heat sink extends laterally over the die to which the heat sink is attached.

18. (original) The thermally enhanced semiconductor package with a stack of dies as claimed in claim 16 wherein the heat sink is electrically grounded.

19. (original) The thermally enhanced semiconductor package with a stack of dies as claimed in claim 16 wherein:

the heat sink has an electrically conductive coating;

one of the dies in the stack of dies is connected to the electrically conductive coating;

and

the electrically conductive coating is connected to a ground plane.

20. (original) The thermally enhanced semiconductor package with a stack of dies as claimed in claim 16 wherein a heat sink is positioned between each adjoining pair of dies in the stack of dies.